



The key to FPGA-ASIC design

Creators of the
Clash compiler



The key to
FPGA design

qbaylogic.com

Clash Formal

Synergizing Functional Programming, Proofs & Hardware Design

Dr. Felix Klein

October 6th, 2025 – FOMSESS Annual Workshop



The key to FPGA-ASIC design

QBayLogic: Our Expertise

- **FPGA based Development:**
IP Design in VHDL, (System)Verilog, **Haskell/Clash**
- **ASIC Design:** RTL Development, Simulation
- **FPGA/ASIC Validation and Verification**
- **Systems-on-Chip:**
QSys, IP-designer Vivado, IP-blocks, LiTex, RISC-V
- **Workflow Design & Setup:** CI / CD / CT / CV
- **(Research related) Product Development:**
from the idea to the first prototype
- **Project Planning & Management**

Member of
**CHIPTECH
TWENTE**



Customers:



Haskell: more than just a programming language



➤ Functional Language

- based on the lambda calculus
- independent from any computer architecture specifics

➤ Strongly Typed

- bugs can be caught even before running the program
- properties can be expressed and ensured via the types

➤ Research Use

- various papers, tools and research about (and using) the language
- popular at universities (introductory courses to computer science)

➤ Industrial Use

- *Meta, Microsoft, Standard Chartered, Tesla, Klarna, Galois, Serokell, ...*
- popular for applications with strong safety and security requirements

Haskell: a versatile all-rounder



- **Web Frameworks:** IHP, Obelisk, Snap, Yesod, ...
- **Build & Package Management:** Cabal, Nix, Shake, ...
- **Embedded & Distributed Systems:** Ivory, Copilot, Cloud Haskell, sparkle, ...
- **Graphics & Music & Art:** Gloss, Diagrams, Haskore, Tidal Cycles, ...
- **Formal Verification**
 - Haskell has its own, built-in, type based constraint system
 - Correctnes Proofs / Proof Assistants: `hs-to-coq`, `agda2hs`, Haskabelle, ...
 - Refinement Types: Liquid Haskell
 - Automated Reasoning: `sbv`, `yices-painless`, ...
- **Hardware:** Bluespec, Lava, ForSyDe, **Clash**
- ...

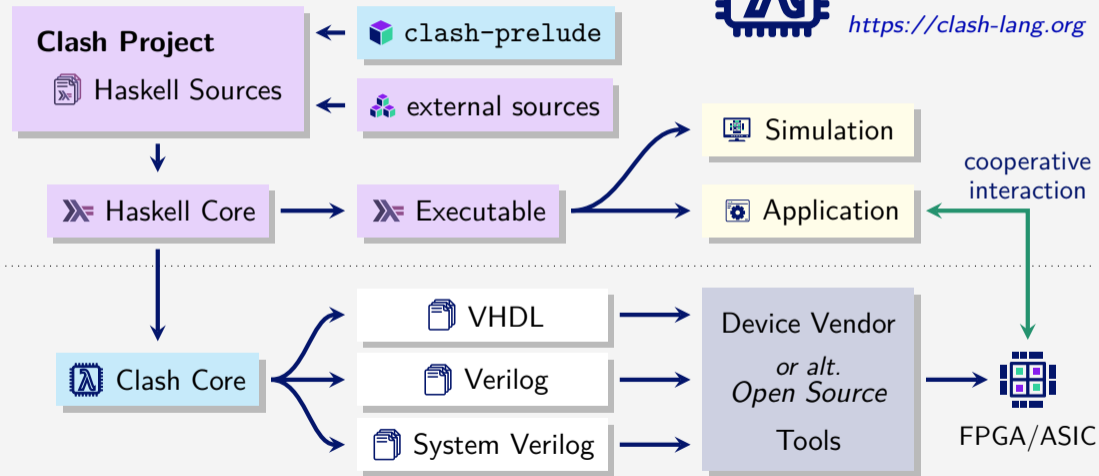
Clash Compiler: Haskell → Hardware



Clash

A modern, functional, open source hardware description language

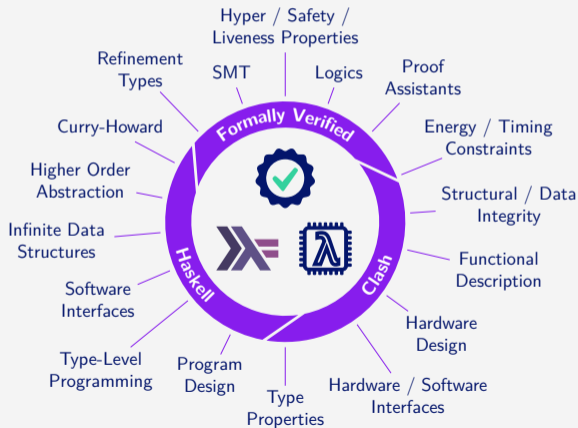
<https://clash-lang.org>





Clash Formal

Ecosystem formally verifiable IT



➤ Proof Assistants

Agda ROCQ LEAN

➤ IFC / SMT / Refinement Types

sec1ib: static information-flow security
 Liquid Haskell

➤ Temporal (Hyper-)Properties

(Hyper) Temporal Stream Logic
 Functional Reactive Programming

➤ Specification Frameworks

Sail RISC-V CHERI

Clash Formal

➤ Research Questions (50%):

- To which extend can we reuse the existing solutions for software verification in Haskell to also verify hardware designs in Clash?
- To which extend can we utilize the type system of Haskell to describe the properties to be verified?
- Can the properties that are specified this way be shared and used in a cooperative manner between the hardware and the software, running on that particular hardware in the end?

➤ Development (50%):

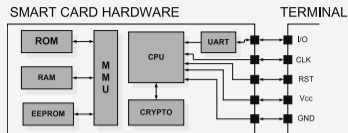
- Extension of the existing Clash compiler / ecosystem with user friendly tools for the construction of formally verified designs (open source).
- Smart Card Demonstrator (open source)

Smart Cards

- dedicated hardware tokens for security relevant data management (passwords, identities, ...)
- hand-held mini-computer: CPU, memory, crypto core, custom operating system
- various applications & interfaces
- open standards:



- **Passkeys** will be the technology of the future!



What the hell are passkeys and why are they suddenly everywhere?

Tech giants call it a "passwordless future," but the switch

What Are Passkeys and Why Are Tech Giants Embracing Them?

CONTRIBUTOR
Chris Morris

PUBLISHED
NOV 15, 2023 9:35AM

Over 400 million Google accounts have used passkeys, but our passwordless future remains elusive

/ Google has seen passkey over a billion times.



Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} \\ 2 \\ \times 6 \\ \hline \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} \\ 6 \\ \times 6 \\ \hline \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} 4 \quad 2 \\ \times 6 \quad 6 \\ \hline \quad 2 \cdot 6 \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} \\ 6 \\ \times 6 \\ \hline \cdot 6 \\ 4 \cdot 6 \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} \\ \\ \hline \cdot 6 \cdot 6 \\ 4 \cdot 6 \cdot 6 \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{array}{r} \\ \\ \\ \times \\ \hline \\ 24 \\ 24 \end{array}$$

Karatsuba Multiplication in a Nutshell

$$0101010 \times 1000010$$

Karatsuba Multiplication in a Nutshell

$$\begin{pmatrix} 0 & 1 & 0 & - & - & - & - \\ + & & & 1 & 0 & 1 & 0 \end{pmatrix} \times \begin{pmatrix} 1 & 0 & 0 & - & - & - & - \\ + & & & 0 & 0 & 1 & 0 \end{pmatrix}$$

Karatsuba Multiplication in a Nutshell

$$\left(\begin{array}{cccccccc} & 0 & 1 & 0 & - & - & - & - \\ + & & & & 1 & 0 & 1 & 0 \end{array} \right) \times \left(\begin{array}{cccccccc} & 1 & 0 & 0 & - & - & - & - \\ + & & & & 0 & 0 & 1 & 0 \end{array} \right)$$

$$\begin{array}{r} (010 \times 100) \text{ -----} \\ + \quad (1010 \times 100) \text{ ----} \\ + \quad (010 \times 0010) \text{ ----} \\ + \quad (1010 \times 0010) \end{array}$$

Karatsuba Multiplication in a Nutshell

$$\begin{pmatrix} 0 & 1 & 0 & - & - & - & - \\ + & & & 1 & 0 & 1 & 0 \end{pmatrix} \times \begin{pmatrix} 1 & 0 & 0 & - & - & - & - \\ + & & & 0 & 0 & 1 & 0 \end{pmatrix}$$

$$\begin{array}{r} (010 \times 100) \text{ -----} \\ + (1010 \times 100) \text{ ----} \\ + (010 \times 0010) \text{ ----} \\ + (1010 \times 0010) \end{array}$$

Multiply x and y :

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{hi} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

Karatsuba Multiplication in a Nutshell

$$(x_{lo} + x_{hi}) \cdot (y_{lo} + y_{hi}) = x_{lo} \cdot y_{lo} + x_{lo} \cdot y_{hi} + x_{hi} \cdot y_{lo} + x_{hi} \cdot y_{hi}$$

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Karatsuba Multiplication in a Nutshell

$$(x_{lo} + x_{hi}) \cdot (y_{lo} + y_{hi}) - x_{lo} \cdot y_{lo} - x_{hi} \cdot y_{hi} = x_{lo} \cdot y_{hi} + x_{hi} \cdot y_{lo}$$

$$\begin{array}{r} (010 \times 100) \text{ -----} \\ + (1010 \times 100) \text{ ----} \\ + (010 \times 0010) \text{ ----} \\ + (1010 \times 0010) \end{array}$$

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$$(x_{lo} + x_{hi}) \cdot (y_{lo} + y_{hi}) - x_{lo} \cdot y_{lo} - x_{hi} \cdot y_{hi} = x_{lo} \cdot y_{hi} + x_{hi} \cdot y_{lo}$$

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Multiply x and y :

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 + \quad (x_{hi} \times y_{hi}) \cdot 2^n \\
 - \quad (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\
 - \quad (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\
 + \quad (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\
 + \quad (x_{lo} \times y_{lo})
 \end{array}$$

Karatsuba Multiplication in a Nutshell

$$O(n^{1.59})$$

$$(x_{lo} + x_{hi}) \cdot (y_{lo} + y_{hi}) - x_{lo} \cdot y_{lo} - x_{hi} \cdot y_{hi} = x_{lo} \cdot y_{hi} + x_{hi} \cdot y_{lo}$$

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Multiply x and y :

$$\begin{array}{r} + (x_{hi} \times y_{hi}) \cdot 2^n \\ - (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ + (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

karatsuba ::

Unsigned n → Unsigned m → Unsigned (n + m)
karatsuba

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

```
karatsuba ::
```

```
  Unsigned n → Unsigned m → Unsigned (n + m)  
karatsuba
```

```
type Low  n = n `Div` 2  
type High n = n - n `Div` 2
```

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

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```
  | SNat :: SNat s ← SNat @(Max n m)
```

```
type Low  n = n `Div` 2  
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Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

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  | SNat :: SNat s ← SNat @(Max n m)
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```
type Low  n = n `Div` 2  
type High n = n - n `Div` 2
```

```
where
```

```
  xlo, ylo :: Unsigned (Low s)
```

```
  xhi, yhi :: Unsigned (High s)
```

```
  (xhi, xlo) = bitCoerce (resize x)
```

```
  (yhi, ylo) = bitCoerce (resize y)
```

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

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```

```
  (xhi, xlo) = bitCoerce (resize x)
```

```
  (yhi, ylo) = bitCoerce (resize y)
```

```
  xs, ys :: Unsigned (High s + 1)
```

```
  xs = resize xlo + resize xhi
```

```
  ys = resize ylo + resize yhi
```

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

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  xhi, yhi :: Unsigned (High s)
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```
  (xhi, xlo) = bitCoerce (resize x)
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```
  (yhi, ylo) = bitCoerce (resize y)
```

```
  xs, ys :: Unsigned (High s + 1)
```

```
  xs = resize xlo + resize xhi
```

```
  ys = resize ylo + resize yhi
```

```
  z0, z1, z2 :: Unsigned (n + m)
```

```
  z0 = resize (karatsuba xlo ylo)
```

```
  z1 = resize (karatsuba xs ys) - z0 - z2
```

```
  z2 = resize (karatsuba xhi yhi)
```

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

karatsuba ::

Unsigned n → Unsigned m → Unsigned (n + m)

karatsuba

| SNat :: SNat s ← SNat @(Max n m)
=

z₂ << SNat @(2 * Low s) + z₁ << SNat @(Low s) + z₀

where

x_{lo}, y_{lo} :: Unsigned (Low s)

x_{hi}, y_{hi} :: Unsigned (High s)

(x_{hi}, x_{lo}) = bitCoerce (resize x)

(y_{hi}, y_{lo}) = bitCoerce (resize y)

x_s, y_s :: Unsigned (High s + 1)

x_s = resize x_{lo} + resize x_{hi}

y_s = resize y_{lo} + resize y_{hi}

z₀, z₁, z₂ :: Unsigned (n + m)

z₀ = resize (karatsuba x_{lo} y_{lo})

z₁ = resize (karatsuba x_s y_s) - z₀ - z₂

z₂ = resize (karatsuba x_{hi} y_{hi})

```
type Low n = n `Div` 2
type High n = n - n `Div` 2
```

Multiply x and y:

$$\begin{aligned}
& (x_{hi} \times y_{hi}) \cdot 2^n \\
+ & (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\
- & (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\
- & (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\
+ & (x_{lo} \times y_{lo})
\end{aligned}$$

```
karatsuba ::
```

```
  ∀ k n m. (KnownNat n, KnownNat m) ⇒ SNat k →  
  Unsigned n → Unsigned m → Unsigned (n + m)
```

```
karatsuba k@SNat x y
```

```
  | SNat :: SNat s ← SNat @(Max n m)  
  =
```

```
    z2 << SNat @(2 * Low s) + z1 << SNat @(Low s) + z0
```

```
where
```

```
xlo, ylo :: Unsigned (Low s)
```

```
xhi, yhi :: Unsigned (High s)
```

```
(xhi, xlo) = bitCoerce (resize x)
```

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(yhi, ylo) = bitCoerce (resize y)
```

```
xs, ys :: Unsigned (High s + 1)
```

```
xs = resize xlo + resize xhi
```

```
ys = resize ylo + resize yhi
```

```
z0, z1, z2 :: Unsigned (n + m)
```

```
z0 = resize (karatsuba k xlo ylo)
```

```
z1 = resize (karatsuba k xs ys) - z0 - z2
```

```
z2 = resize (karatsuba k xhi yhi)
```

```
type Low n = n `Div` 2  
type High n = n - n `Div` 2
```

Multiply x and y:

$$\begin{aligned} & (x_{hi} \times y_{hi}) \cdot 2^n \\ + & (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - & (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - & (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + & (x_{lo} \times y_{lo}) \end{aligned}$$

```
karatsuba ::
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  ∀ k n m. (KnownNat n, KnownNat m) ⇒ SNat k →  
  Unsigned n → Unsigned m → Unsigned (n + m)
```

```
karatsuba k@SNat x y
```

```
  | SNat :: SNat s ← SNat @(Max n m)  
  = case compareSNat (SNat @(n + m)) k of  
    SNatLE → resize x * resize y  
    SNatGT → z2 << SNat @(2 * Low s) + z1 << SNat @(Low s) + z0
```

```
where
```

```
xlo, ylo :: Unsigned (Low s)  
xhi, yhi :: Unsigned (High s)  
(xhi, xlo) = bitCoerce (resize x)  
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xs, ys :: Unsigned (High s + 1)  
xs = resize xlo + resize xhi  
ys = resize ylo + resize yhi
```

```
z0, z1, z2 :: Unsigned (n + m)  
z0 = resize (karatsuba k xlo ylo)  
z1 = resize (karatsuba k xs ys) - z0 - z2  
z2 = resize (karatsuba k xhi yhi)
```

```
type Low n = n `Div` 2  
type High n = n - n `Div` 2
```

Multiply x and y:

$$\begin{array}{r} (x_{hi} \times y_{hi}) \cdot 2^n \\ + (x_{lo} + x_{hi}) \times (y_{lo} + y_{hi}) \cdot 2^{\frac{n}{2}} \\ - (x_{lo} \times y_{lo}) \cdot 2^{\frac{n}{2}} \\ - (x_{hi} \times y_{hi}) \cdot 2^{\frac{n}{2}} \\ + (x_{lo} \times y_{lo}) \end{array}$$

```
karatsuba ::
  ∀ k n m. (KnownNat n, KnownNat m) ⇒ SNat k →
  Unsigned n → Unsigned m → Unsigned (n + m)
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```
type Low n = n `Div` 2
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```
karatsuba k@SNat x y
  | SNat :: SNat s ← SNat @(Max n m)
  = case compareSNat (SNat @(n + m)) k of
      SNatLE → resize x * resize y
      SNatGT → z2 << SNat @(2 * Low s) + z1 << SNat @(Low s) + z0
```

where

```
xlo, ylo :: Unsigned (Low s)
xhi, yhi :: Unsigned (High s)
(xhi, xlo) = bitCoerce (resize x)
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```
xs, ys :: Unsigned (High s + 1)
xs = resize xlo + resize xhi
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```
z0, z1, z2 :: Unsigned (n + m)
z0 = resize (karatsuba k xlo ylo)
z1 = resize (karatsuba k xs ys) - z0 - z2
z2 = resize (karatsuba k xhi yhi)
```

n	(*) @ (Unsigned n) [# gates]	karatsuba @8 @n @n [# gates]
8	569	993
16	2536	4118
32	10437	14898
64	41924	49220
128	166252	156578
256	661993	487583
512	2638547	1496300

$O(n^{1.59})$

karatsuba ::

$\forall k\ n\ m. (\text{KnownNat } n, \text{KnownNat } m) \Rightarrow \text{SNat } k \rightarrow$
 $\text{Unsigned } n \rightarrow \text{Unsigned } m \rightarrow \text{Unsigned } (n + m)$

karatsuba k@SNat x y

| SNat :: SNat s \leftarrow SNat @(Max n m)
= case compareSNat (SNat @(n + m)) k of
 SNatLE \rightarrow resize x * resize y
 SNatGT \rightarrow z₂ \ll SNat @(2 * Low s) + z₁ \ll SNat @(Low s) + z₀

where

x_{lo}, y_{lo} :: Unsigned (Low s)
x_{hi}, y_{hi} :: Unsigned (High s)
(x_{hi}, x_{lo}) = bitCoerce (resize x)
(y_{hi}, y_{lo}) = bitCoerce (resize y)

x_s, y_s :: Unsigned (High s + 1)
x_s = resize x_{lo} + resize x_{hi}
y_s = resize y_{lo} + resize y_{hi}

z₀, z₁, z₂ :: Unsigned (n + m)

z₀ = resize (karatsuba k x_{lo} y_{lo})

z₁ = resize (karatsuba k x_s y_s) - z₀ - z₂

z₂ = resize (karatsuba k x_{hi} y_{hi})

```
type Low n = n `Div` 2
type High n = n - n `Div` 2
```

n	(*) @(Unsigned n) [# gates]	karatsuba @8 @n @n [# gates]
8	569	993
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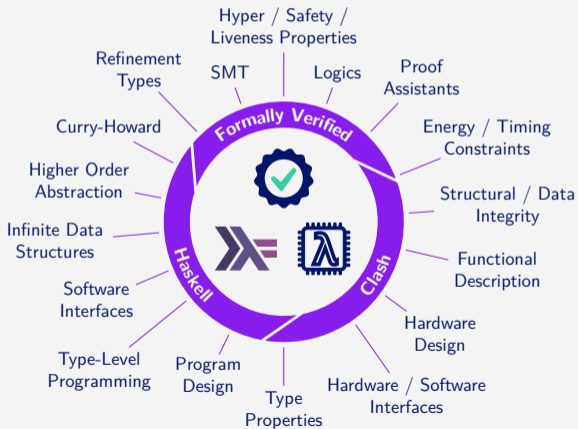


$O(n^{1.59})$



Clash Formal

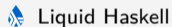
Ecosystem formally verifiable IT



Proof Assistants



SMT / Refinement Types



Specification Frameworks



<https://clash-formal.org>